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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Frampton E. ELLIS, III Conf. No. 2622
SERIAL NUMBER: 10/663,911 Group: 2153
FILED: August 25, 2003 Examiner: Dung C. DING
FOR: GLOBAL NETWORK COMPUTERS FOR SHARED
PROCESSING

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Assistant Commissioner for Patents
PO BOX 1450
ALEXANDRIA, VA 22313-1450

March 29, 2007

Sir:

Applicant(s) wish(es) to disclose the following information.

REFERENCES

Applicant(s) wish(es) to make of record the documents listed on the attached Form PTO-1449. Copies of the listed documents are attached, where required, as are either statements of relevancy or any readily available full or partial English translations of any non-English-language documents.

EXPLANATION OF RELEVANCE

RELATED CASES

Attached is a list of Applicant's(s') pending applications and issued patents which may be related to the present application. Copies of the documents, where required, are attached along with Form PTO-1449.

CERTIFICATION

The undersigned certifies that

- each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign or international patent office in a counterpart foreign or international application for the first time (to the knowledge of the undersigned, having made reasonable inquiry) not more than three months prior to the filing of this statement (See 37 CFR 1.97(e)(1)).
- no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign or international patent office in a counterpart foreign or international application, and, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement. (See 37 CFR 1.97(e)(2))

BASIS FOR CONSIDERATION

This Information Disclosure Statement is filed:

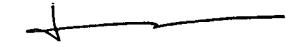
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- without fee and within three months of the filing date of the application.
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- without fee and within three months of the date of entry of the U.S. national stage.
- without fee and before the mailing date of a first Office Action on the merits (to the knowledge of the undersigned).
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- without fee and with the appropriate certification above.
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- without fee and with a new CPA application.
-
- without fee and with a Request for Continued Examination.
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- with fee and before the mailing date of any Final Office Action, Notice of Allowance or an action that otherwise closes prosecution (to the knowledge of the undersigned).
-
- with fee, appropriate certification above, and before payment of the Issue Fee.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith, or credit any overpayment to Deposit Account No. 50-3266.

Respectfully submitted,

DLA PIPER US LLP



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Substitute for form 1449A/PTO		<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/663,911
		Filing Date	August 25, 2003
		First Named Inventor	Frampton E. ELLIS, III
		Art Unit	2153
		Examiner Name	Dung C. DING
Sheet	1	of	2
		Attorney Docket Number	
		313449-P0017 C1	

~~CONFIDENTIAL~~ CONFIDENTIAL DOCUMENTS

FOREIGN PATENT DOCUMENTS

•EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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				Art Unit	2153
				Examiner Name	Dung C. DING
				Attorney Docket Number	313449-P0017 C1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Foster et al., "The Grid: Blueprint for a New Computing Infrastructure", Morgan Kaufman Publishers, Inc., 1998.	
		Hwang et al., "Scalable Parallel Computing", WCB McGraw-Hill, 1998.	
		Patterson et al., "Computer Architecture: A Quantitative Approach" (2nd Edition), Morgan Kaufmann Publishers, Inc., 1996.	
		Culler et al., "Parallel Computer Architecture", Morgan Kaufmann Publishers, Inc., 1998.	
		Hennessy et al., "Computer Organization and Design", Morgan Kauffmann Publishers, Inc., 1998.	
		Steinert-Threlkeld; "NEW BREED OF CHIP TI develops a super circuit"; The Sun Baltimore; May 4, 1992	
		Dallas Morning News; "LSI holds big plans for tiny chips Versatility of 'system on a chip' creates niche in microelectronics;" March 4, 1996	
		Mokhoff; "System-on-a-chip comes to wireless arena;" Electronic Engineering Times; Feb 12, 1996	
		Cindi; "System on a Chip' stars at ISSCC;" Electronic News; Feb 19, 1996	
		Ang; "System-on-a-chip to define next-generation set-top box"; Electronic Engineering Times; Dec 15, 1995	
		Marc; "New family of microprocessor cores from LSI Logic extends customers' system-on-a-chip design capability" Nov 7, 1994	
		Wall Street Journal; "Technology Brief – Advance Micro Devices Inc.: Company unveils Microchip for Hand-Held Computers"; Oct 18, 1993	
		Gelsinger, Patrick et al. "Microprocessors circa 2000," IEEE Spectrum, October 1989 pp. 43-47.	
		Yu, Albert. "The Future of Microprocessors," IEEE Micro, December 1996, pp. 46-53.	
		McWilliams. "Dell to Phase Out Computers Using Intel's Itanium," The Wall Street Journal, Online, September 15, 2005.	
Examiner Signature	Date Considered		

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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